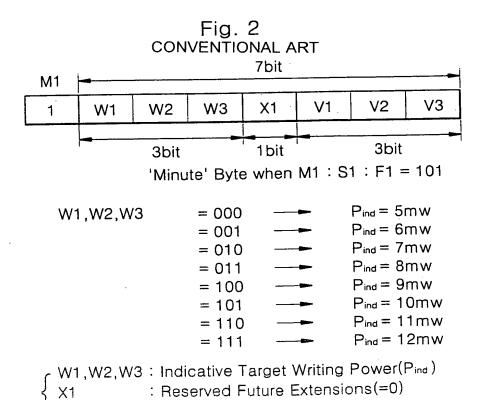
Fig. 1 40 **INPUT** 50 DATA CHANNEL **OPTICAL** BIT 10 **DIGITAL** DRIVER **ENCODER** 30a RECORDING 60 SIGNAL **PROCESSING** DIGITAL R/F REPRODUCING 30b SIGNAL T.E **SERVO** , LED **PROCESSING** F.E 11 100 70  $\square(M)$ DRIVE **MICOM** REPRODUCED KEY **INPUT** DATA 80



V1, V2, V3: Reference Speed



Fig. 3 CONVENTIONAL ART

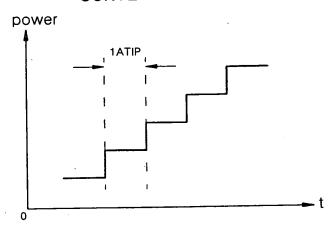


Fig. 4 CONVENTIONAL ART

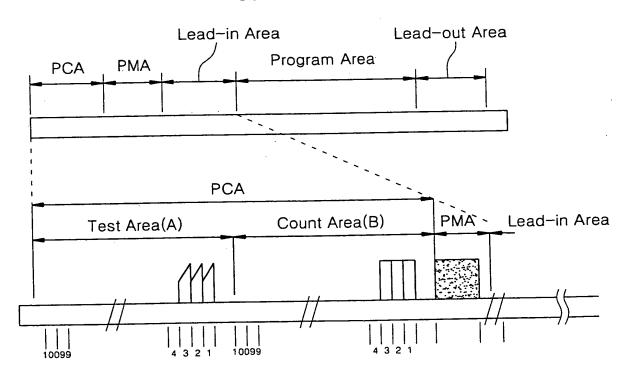




Fig. 5 CONVENTIONAL ART

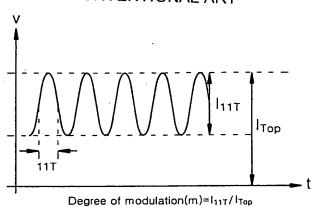
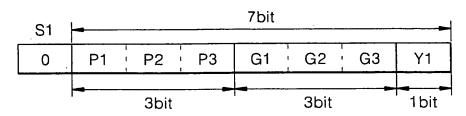


Fig. 7
CONVENTIONAL ART



'Second' Byte when M1 : S1 : F1 = 001

P1,P2,P3: Power multiplication factor p at reference speed

G1,G2,G3: Target  $m{r}$  value of the modulation/power function for all speeds

Y1 : Reserved for future exetentions(=0000)



Fig. 8 CONVENTIONAL ART

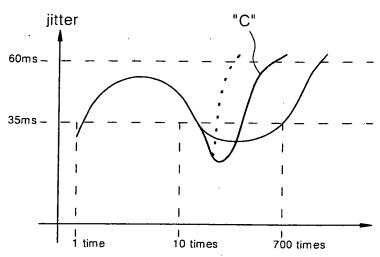


Fig. 9 CONVENTIONAL ART

	PRECEDING RECORDING	FOLLOWING RECORDING	REPRODUCTION CHARACTERISTICS
RECORDING POWER	HIGH	LOW	BAD
	HIGH	HIGH	NORMAL
	LOW	HIGH	GOOD
	LOW	LOW	NORMAL